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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/738,720	12/15/2000	Stig Halvarsson	03964-14 (6563/6008)	1466
7590	04/05/2005		EXAMINER BLOUNT, STEVEN	
Donald I. Bartels Coudert Brother LPP Two Palo Alto Square 3000 El Camino Real, 4th Floor Palo Alto, CA 94306-2121			ART UNIT	PAPER NUMBER
			2661	

DATE MAILED: 04/05/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

09/738,720

Applicant(s)

HALVARSSON ET AL.

Examiner

Steven Blount

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-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 12 October 2004.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1 - 11 is/are pending in the application.
- 4a) Of the above claim(s) _____ is/are withdrawn from consideration.
- 5) ☒ Claim(s) 7 is/are allowed.
- 6) ☒ Claim(s) 1 - 5, 8 - 11 is/are rejected.
- 7) ☒ Claim(s) 6 is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☐ The drawing(s) filed on _____ is/are: a) ☐ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
- ☐ Certified copies of the priority documents have been received.
 - ☐ Certified copies of the priority documents have been received in Application No. _____.
 - ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☒ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Claim Rejections - 35 USC § 103

1. Claims 1 – 5 and 8 – 10 are rejected under 35 U.S.C. 103(a) as being obvious over U.S. patent 6,215,722 to Park in view of U.S. patent 5,649,179 to Steenstra et al.

With regard to claim 1, Park teaches a multiplexable data stream delay line 13/15 (see figure 1) where the delay line is a shift register unit (corresponding to what is described in applicants specification), and the multiplexer parses the data as is described in col 3, lines 13, 23 – 25, and 40 – 57 through the use of register decoder unit 9. Park also teaches a plurality of processing units as shown in figure 1, and also teaches that the data is sent to a synchlink dynamic random access memory (SDRAM).

Park does not, however, explicitly teach that the register decoder 9, (which enables the multiplexer 15 to “select(s) the write signal having the wanted delay” (col 3, lines 40+)) to be controlled by an interchangeable program.

Steenstra et al teaches a programmable decoder, wherein the decoder is configured through the use of a configuration file. See col 4 lines 20+, the abstract, and also figure 4 (program memory 44), figure 5 (program 72 and program instructions 71).

It would have been obvious to one of ordinary skill in the art at the time of the invention to have provided Park with a programmable decoder wherein the decoder is controlled by an interchangeable program, in order to provide a means for automatically and flexibly change the portions of the data flow which are being parsed.

With regard to claim 2, see the discussion of the multiplexer 15 above.

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With regard to claim 3, the multiplexer 15 keeps track of the data in the delay line, and would enable program in the decoder to become operational when data is located in the delay line.

With regard to claim 4, it would be obvious to use a 23 shift deep and 1 byte wide shift register, as the length of the shift register in applications such as this are typically of this order.

With regard to claim 5, the register data mentioned in col 3 line 42 would be obvious to be held in a "position register", and note the program in steenstra et al would have the decoding operation occur in a "predetermined way".

With regard to claim 8, see the discussion of registers above, and also note The use of shift registers 13 in Park et al.

With regard to claim 9, it would be obvious to implement the memory of Steenstra et al in a stack form.

With regard to claim 10, the program in Steenstra et al could recognize a pattern even if it started at a different position, and note the discussion of registers above.

2. Claim 11 is rejected under 35 U.S.C. 103(a) as being obvious over U.S. patent 6,215,722 to Park in view of U.S. patent 5,649,179 to Steenstra et al as applied to claims 1 – 5 and 8 – 10 above, and further in view of U.S. patent 4,489,381 to Lavalley et al.

Park/Steenstra et al teach the invention as discussed above, but do not teach the use of double ported memory. This is taught in Lavalley et al. It would have been obvious to one of ordinary skill in the art at the time of the invention to have used double ported

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memory in Park/Steenstra et al, in light of the teachings of Lavallo et al, in order to provide a means for increasing the rate at which the decoder instructions may be processed.

3. Claim 6 is objected to as being dependent upon a rejected base claim, but would be allowable if rewritten to include the limitations of the base claim and any intervening claims. Claim 7 is allowed.

4. Applicants arguments are moot in view of the new grounds of rejection.

5. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the mailing date of this final action.

6. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Steven Blount whose telephone number is 703-305-0319. The examiner can normally be reached on M-F 9:00 - 5:30.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Mr. Chau Nguyen, can be reached on 571 - 272 - 3071. The fax phone

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number for the organization where this application or proceeding is assigned is 703-872-9306.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).


Ajit Patel
Primary Examiner

SB


3/22/05